OOH
1-31-06
BAST

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3	"4682284".pn. "5954815".pn. "4841476".pn.	US-PGPUB; USPAT	OR	OFF	2006/01/27 14:36
S2	0	microinstruction near5 invalid$4 with (part outside separate$1) near4 macroinstruction	US-PGPUB; USPAT	OR	ON	2005/07/05 16:14
S3	0	microinstruction with invalid$4 with (part outside separate$1) with macroinstruction	US-PGPUB; USPAT	OR	ON	2005/07/05 16:15
S4	25	invalid$5 near3 microinstruction	US-PGPUB; USPAT	OR	ON	2005/07/06 13:11
S5	6	invalid$5 near3 microinstruction and trac$3	US-PGPUB; USPAT	OR	ON	2005/07/06 13:12
S6	3	invalid$5 near3 microinstruction and trace	US-PGPUB; USPAT	OR	ON	2005/07/06 13:15
S7	11	invalid$5 with microinstruction and trace	US-PGPUB; USPAT	OR	ON	2005/07/06 13:19
S8	35	invalid$5 with $5instruction same trace	US-PGPUB; USPAT	OR	ON	2005/07/06 13:20
S9	31	invalid$5 with $5instruction same trace and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2005/07/06 13:44
S10	75	branch near3 macroinstruction and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2005/07/06 13:44
S11	72	branch near2 macroinstruction and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2005/07/06 13:44
S12	150	(writ$3 stor$3) with trace with (micro-instruction microinstruction instruction) with (queue FIFO buffer)	US-PGPUB; USPAT	OR	ON	2006/01/27 14:37
S13	13	(writ$3 stor$3) with trace with (micro-instruction microinstruction instruction) with (queue FIFO buffer) and overwrit$3 with (micro-instruction microinstruction instruction)	US-PGPUB; USPAT	OR	ON	2006/01/27 14:39
S14	0	overwrit$3 with (micro-instruction microinstruction instruction) with outside with trace	US-PGPUB; USPAT	OR	ON	2006/01/27 14:39
S15	11	(micro-instruction microinstruction instruction) with outside with trace same (queue FIFO buffer)	US-PGPUB; USPAT	OR	ON	2006/01/27 14:41
S16	0	(micro-instruction microinstruction instruction) with ("not in" "not inside" "not within") with trace same (queue FIFO buffer)	US-PGPUB; USPAT	OR	ON	2006/01/27 14:42

S18	24	(micro-instruction microinstruction instruction) with (outside "not in" "not inside" "not within") with trace	US-PGPUB; USPAT	OR	ON	2006/01/27 14:42
S19	13	S18 not S15	US-PGPUB; USPAT	OR	ON	2006/01/27 14:48
S20	11	overwrit$3 with invalid near4 (microinstruction instruction micro-instruction) with (queue FIFO buffer)	US-PGPUB; USPAT	OR	ON	2006/01/30 10:13
S21	13	replac$3 with invalid near4 (microinstruction instruction micro-instruction) with (queue FIFO buffer)	US-PGPUB; USPAT	OR	ON	2006/01/30 08:12
S25	160	prefetch$3 near5 (buffer queue) same (valid$4 invalid$4) and pointer same (valid$4 invalid$4)	US-PGPUB; USPAT	OR	ON	2006/01/30 10:06
S26	106	prefetch$3 near5 (buffer queue) same (valid$4 invalid$4) same pointer	US-PGPUB; USPAT	OR	ON	2006/01/30 10:06
S27	96	prefetch$3 near5 (buffer queue) same (valid$4 invalid$4) same pointer same instruction	US-PGPUB; USPAT	OR	ON	2006/01/30 10:10
S28	1	prefetch$3 near5 (buffer queue) same (valid$4 invalid$4) same pointer same instruction same overwrit$4	US-PGPUB; USPAT	OR	ON	2006/01/30 10:07
S29	27	instruction same (buffer queue) same tag$4 with (valid$4 invalid$4) same pointer same $4writ$3	US-PGPUB; USPAT	OR	ON	2006/01/30 10:11
S30	47	overwrit$3 with invalid near4 (microinstruction instruction micro-instruction)	US-PGPUB; USPAT	OR	ON	2006/01/31 08:21
S31	10	("4847755" | "5155843" | "5627983" | "5822559" | "5870578" | "5872946" | "6112019" | "6115807" | "6148394" | "6182210").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/30 10:23
S32	43	compact$3 with (queue buffer) with instruction	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/30 10:26
S33	406	compress$3 with (queue buffer) with instruction	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/30 10:26
S34	16	compress$3 with (queue buffer) with instruction same (invalid$4 valid$4)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/30 11:18

S35	5	compact$3 with (queue buffer) with instruction same invalid$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/31 08:21
S36	0	advanc$3 same (write head tail) near5 pointer same overwrit$3 same (queue buffer) same instruction same (invalid$4 valid$4)	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/30 11:19
S37	6	(write head tail) near5 pointer same overwrit$3 same (queue buffer) same instruction same invalid$4	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/30 11:19
S38	31	replace$4 with invalid$4 with (microinstruction instruction micro-instruction) same (queue buffer FIFO)	US-PGPUB; USPAT	OR	ON	2006/01/31 08:21
S39	6	(buffer queue) with trace with (microinstruction instruction) with invalid$4	US-PGPUB; USPAT	OR	ON	2006/01/30 15:03
S40	27	trace with (microinstruction instruction) with invalid$4	US-PGPUB; USPAT	OR	ON	2006/01/31 08:19
S41	6	trace same (microinstruction instruction) same invalid$4 same mispredict$3	US-PGPUB; USPAT	OR	ON	2006/01/30 15:08
S42	0	trace same path same invalid$4 same mispredict$3	US-PGPUB; USPAT	OR	ON	2006/01/30 15:08
S43	7	trace same invalid$4 same mispredict$3	US-PGPUB; USPAT	OR	ON	2006/01/31 08:19
S44	24	trace same (invalid$4 flush$3) same mispredict$3	US-PGPUB; USPAT	OR	ON	2006/01/30 15:33
S45	17	trace same (invalid$4 flush$3) same predict$3 with (incorrect$2 wrong$2)	US-PGPUB; USPAT	OR	ON	2006/01/31 08:18
S47	84	belong with trace and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2006/01/30 15:56
S48	35	belong near4 trace and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2006/01/30 15:57
S49	125	outside near4 trace and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2006/01/30 15:58
S50	49	outside adj4 trace and g06f$.ipc.	US-PGPUB; USPAT	OR	ON	2006/01/31 08:17
S51	1	"6094729".pn.	US-PGPUB; USPAT	OR	OFF	2006/01/31 08:14
S52	211	invalidat$3 with fetch$2 with instruction	US-PGPUB; USPAT	OR	ON	2006/01/31 08:15
S53	65	invalidat$3 with fetch$2 with instruction same (buffer queue)	US-PGPUB; USPAT	OR	ON	2006/01/31 08:16

S55	0	invalidat$3 with fetch$2 with instruction with ("too many" "exceed")	US-PGPUB; USPAT	OR	ON	2006/01/31 08:17
S56	91	outside adj4 trace	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 08:18
S57	14	outside adj4 trace and g06f$.ipc.	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 08:17
S58	0	trace same (invalid$4 flush$3) same predict$3 with (incorrect$2 wrong$2)	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 08:18
S59	0	trace same invalid$4 same mispredict$3	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 08:19
S60	4	trace with (microinstruction instruction) with invalid$4	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 08:19
S61	0	replace$4 with invalid$4 with (microinstruction instruction micro-instruction) same (queue buffer FIFO)	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 08:21
S62	0	compact$3 with (queue buffer) with instruction same invalid$4	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 08:21
S63	0	overwrit$3 with invalid near4 (microinstruction instruction micro-instruction)	USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/31 09:23
S64	9	(eliminat$3 invalidat$3 remov$3 replac$4) with non-sequential with instruction	US-PGPUB; USPAT	OR	ON	2006/01/31 09:24